



Handbook of Fine Pitch Surface Mount Technology (Electrical Engineering)

John H. Lau

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Surface mount technology (SMT) is a mature technology. SMT allows placement of more surface mount components (SMC) into smaller and tighter printed circuit board (PCB) areas. This increased density means increased performance and power in smaller packaging systems, and allows manufacturing of smaller and higher performance products at lower cost. The advance of integrated circuit (IC) technology and the requirements of high density for high-speed circuitry is driving the design of SMC to higher pin count and smaller package size. In general, the higher pin count and smaller package size are accomplished by reducing the bond pad size and spacing (pitch) on the chip level and the lead/pin/solder dimensions and pitch on the chip carrier (module) level. The last few years have witnessed an explosive growth in the research and development efforts devoted to FPT as a direct result of the rapid growth of SMT and miniaturization. Some examples are: hand held lightweight video recorders that can take sharp pictures, hand held lightweight devices that can track the worldwide package movements, and portable computers with tiny yet powerful microprocessors and large memory capability that can fit into a briefcase or into the palm of your hand.

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